

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3226	375/376	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/20 08:33
S2	1364	375/373	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 08:39
S3	773	375/376 and 375/373	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:34
S4	14993	phase adj1 lock adj1 loop\$1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:13
S5	1298	phase adj1 lock adj1 loops	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:13
S6	0	375/373 adj1 phase adj1 lock adj1 loops	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:14
S7	66	two adj1 phase adj1 lock adj1 loops	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:15
S8	3	two adj1 phase adj1 lock adj1 loops and burst adj mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:27
S9	0	clock adj signal adj recover with burst adj mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:28
S10	0	clock adj signal adj recover and burst adj mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:28
S11	8	clock adj signal adj recover and PLL\$1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:39
S12	882	327/165	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:55

S13	15	375/376 and 375/373 and 327/165	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 09:55
S14	669	375/376 and 375/373 and @ay<"2000"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:14
S15	1893	PLLs	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:14
S16	0	clock adj recover and PLLa	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:14
S17	11	clock adj recover and PLLs	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:17
S18	4	"5717730"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:19
S19	6	"6215835"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:19
S20	13	"4234852"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:41
S21	38	"5072195"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 10:41
S22	4	two adj1 phase adj1 lock adj1 loops with variable adj oscillator	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:11
S23	0	two adj1 phase adj1 lock adj1 loops with variable adj oscillator	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 12:56
S24	2	two adj1 phase adj1 lock adj1 loops and variable adj oscillator	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 13:22

S25	13	"575872"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 13:22
S26	7	"5757872"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/06 13:22
S27	39	two adj1 phase adj1 lock adj1 loops and switch	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:16
S28	45	two adj1 phase adj1 lock adj1 loops and (switch or gate)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:29
S29	0	gated adj two adj1 phase adj1 lock adj1 loops	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:29
S30	2	two adj1 phase adj1 lock adj1 loops with switch	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:31
S31	0	two adj1 phase adj1 lock adj1 loops with selection	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:31
S32	11	two adj1 phase adj1 lock adj1 loops with mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:33
S33	305	375/376 and 375/373 and switch	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:34
S34	1	375/376 and 375/373 and two adj1 PLL and switch	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:34
S35	1	375/376 and 375/373 and two adj1 PLLs and switch	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 08:37
S36	13	"5727037"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 10:36

S37	25	PLL with bias\$2 adj signal\$1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/07 10:37
S38	1022	second adj1 pll	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/19 16:09
S39	551	second adj1 pll and (gate or switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/19 16:10
S40	94	second adj1 pll and (gate or switch) and bias\$2 adj1 (voltage or current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/19 16:14
S41	46	"2" adj1 pll and (gate or switch) and bias\$2 adj1 (voltage or current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/19 16:18
S42	87	(first and second) adj1 pll and (gate or switch) and bias\$2 adj1 (voltage or current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/19 16:19
S43	87	(first and second) adj1 pll and (gate or switch) and bias\$2 adj1 (voltage or current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/19 16:19
S44	61	(switch or gate\$1) with second adj1 pll	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/20 08:41
S45	2	(switch or gate\$1) with second adj1 pll and bias\$2 adj1 (voltage or current)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/20 08:42
S47	16563	(second adj1 PLL) or (phase adj1 lock adj1 loop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:12
S48	16563	((second adj1 PLL) or (phase adj1 lock adj1 loop))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:25

S49	163	((second adj1 PLL) or (phase adj1 lock adj1 loop)) with bias\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:33
S50	6	((second adj1 PLL) ) with bias\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:37
S51	0	((second adj1 PLL) ) with bias\$3 with burst	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:37
S52	2	((second adj1 PLL) ) with bias\$3 and burst	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:37
S53	78	((second adj1 PLL) ) and burst	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:38
S55	19	((second adj1 PLL) ) with burst	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:42
S56	6	((second adj1 PLL) ) with bias\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:42
S57	1	((slave adj1 PLL) ) with bias\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:42
S58	1	(slave adj1 (PLL or (phase adj1 lock adj1 loop)) ) with bias\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:43
S59	6	(second adj1 (PLL or (phase adj1 lock adj1 loop)) ) with bias\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:48
S60	49	(second adj1 (PLL or (phase adj1 lock adj1 loop)) ) with mode\$1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 10:48
S61	2	(second adj1 (PLL or (phase adj1 lock adj1 loop)) ) with mode\$1 and bias\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 12:57

S62	1	"20040247050"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 13:25
S63	0	"20040247050" and (first adj1 order)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 13:25
S64	0	"20040247050" and (first adj loop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/21 13:26